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Patents

Patents **1 - 10** on **prevent non-secured mode**. **(0.36** seconds)

Did you mean: ***prevent non-secure mode***

Modular transaction terminal
US Pat. 6065679 - Filed Sep 6, 1996 - IVI Checkmate Inc.
When the DS5002 is not present, this bit should remain cleared to **prevent** unwanted
... When this bit is high, and the ASIC is in its **non-secured mode**, ...

Keyless ignition system with delayed security
US Pat. 5821631 - Filed Feb 7, 1997 - Clark Equipment Company
Assuming that **non-secured** starting is not available, program flow returns back
to block 100 and the microprocessor returns to the "sleep secured **mode**. ...

Multi-level security network system
US Pat. 6304973 - Filed Aug 6, 1998 - Cryptek Secure Communications, LLC
... device in a bypass or **non- secured mode** and thereby network non-sensitive ...
Hence, the principal's profile is preferably configured to **prevent** access ...

Scheme to allow two computers on a network to upgrade from a **non-secured** to a **secured** session ...
US Pat. 5822434 - Filed Jun 18, 1996 - Sun Microsystems, Inc.
... communications between host 1 and the target host into the clear **mode**. ...
large value for the default TTL would **prevent** host 1 from speaking with a ...

Data processor systems
US Pat. 5406624 - Filed Sep 2, 1993 - Algorithmic Research Ltd.
... and the invention is there- **prevent** the compromise of security information to be
... to isolate the puter to perform the **non-secured** activities. ...

Keyless ignition system with washout feature
US Pat. 6236120 - Filed Oct 9, 1998 - Clark Equipment Company
The system 10 has five operating states or modes that include a "secured sleep
mode," a "input code **mode**," a "vehicle run **mode**," a "**non-secured**, timed, ...

Integrated impact detection and alarm system
US Pat. 4942386 - Filed Dec 16, 1988
However, these detectors are simply to **prevent** a player from ... one end and
having a freely movable **non-secured** tapered conical shape at the opposite end. ...

Arrangement of discrete structural parts, especially heat exchanger
US Pat. 4552209 - Filed Mar 25, 1985 - MTU-Motoren-und Turbinen-Union Muenchen GmbH
In order to **prevent** fretting corrosion between the separate parts, ... in the
area of a **non-secured** location of the tubes by an outer supporting frame 3, ...

Pivot for rear seat track fitting

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Patents

Patents **1 - 10** on **prevent non-secure mode**. (0.08 seconds)

Automatic garage door operator with remote load control
US Pat. 4929877 - Filed May 23, 1989
... said controller means having a secure **mode**, a **non- secure mode**, ... means is
in said **non-secure mode**, inhibiting said door actuator to **prevent** operation ...

Encrypted communication system
US Pat. 5222136 - Filed Jul 23, 1992 - Crest Industries, Inc. ·
RAM 142 preferably comprises aa **non-secure mode**, "PUB NET," indicating that ...
communica- potting compound 134 to **prevent** discovery of the in- tions can be ...

Encryption/decryption apparatus with **non**-accessible table of keys
US Pat. 5237611 - Filed Jul 23, 1992 - Crest Industries, Inc.
The potting compound is sufficiently hard and resistant to abrasion to **prevent**
its removal without damaging the components comprising the EDU or at least ...

Bypass apparatus for use in **secure** communication systems
US Pat. 4368357 - Filed.Nov 14, 1980 - International Telephone and Telegraph Corporation
As one can ascertain, such 55 **non-secure mode** and for deactivating said gating
means telephone systems are extremely useful in modern day during a secure ...

Nonvolatile/battery-backed key in PLD
US Pat. 6366117 - Filed Nov 28, 2000 - Xilinx, Inc.
This state monitors a change 30 from secure to **non-secure mode** of the ... the load
and shift signals on lines 104 and 105 inactive to **prevent** readback. ...

Programmable logic device with method of preventing readback
US Pat. 6981153 - Filed Nov 28, 2000 - Xilinx, Inc.
If key memory 23 is operating in **non-secure mode**, the 64-bit words can be read from
... The FPGA can be tested in this **non-secure mode** by using 56 bits of a ...

Programmable logic device with decryption and structure for preventing design relocation
US Pat. 7117372 - Filed Nov 28, 2000 - Xilinx, Inc.
If key memory is operating in **non-secure mode**, the 64-bit words can be read from
... 20 The FPGA can be tested in this **non-secure mode** by using 56 bits of a ...

System and method for managing multiple users with different privileges in an open metering ...
US Pat. 5742683 - Filed Dec 19, 1995 - Pitney Bowes Inc.
Examples of systems for generating modes: normal user **mode**, ... the different
functionality's of the provides security to **prevent** altering of the printed ...

Programmable logic device with partial battery backup
US Pat. 6441641 - Filed Nov 28, 2000 - Xilinx, Inc.
If key memory 23 is operating in **non-secure mode**, the 64-bit words can be read from
... The FPGA can be tested in this **non-secure mode** by using 56 bits of a ...

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Patents

Patents **1 - 1** on **prohibit non-secure mode**. (**0.14** seconds)

Virtual private switched telephone network
US Pat. 6735291 - Filed Nov 10, 2000 - SecureLogix Corporation
And while there are policies that specifically **prohibit** it, sensitive material
can be inadvertently discussed on **non-secure** phones and distributed across ...

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